

Curriculum Vitae

date: 2010-12-30
name: Mario Blunk
day of birth: December 21st 1974
place of birth: 37308 Heilbad Heiligenstadt / Germany
confession: I'm a follower of Jesus Christ
marriage: since 19.08.2006 with Anne Blunk
children: two wonderful daughters

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military service: 1997-1998 German Navy in Bremerhaven and Flensburg

education: 1981 – 1991 secondary school Bodenrode / Germany
1991 – 1995 professional as Electronic Installer at German Railways
in Erfurt / Germany
2003 – 2008 Diploma (Diplomingenieur) of Electrical and Computer
Engineering at University of Applied Sciences Jena / Germany

employments: 1998 – 2000 hard and software development for
IE&MT GmbH & Ko. KG in Erfurt / Germany
2000 – 2006 application engineer for Boundary-Scan/JTAG System CASCON
for Goepel electronic GmbH in Jena / Germany
2007-2008 programming Boundary-Scan systems for Voigt electronic
GmbH in Erfurt / Germany

running own business: since 2009 teaching, constructing, consulting and administration in those
fields:

- ◆ electronics and computer engineering
- ◆ Cadsoft EAGLE hardware design
- ◆ programming microcontrollers and processors Z80, 8051, Ix86, PIC16F84
- ◆ Verilog HDL logic synthesis with Xilinx System ISE
- ◆ Linux, UNIX, MS-Windows
- ◆ Boundary-Scan/JTAG System CASCON (Goepel electronic) Std IEEE1149.1/4/6

languages: German (native) and English (fluently)

